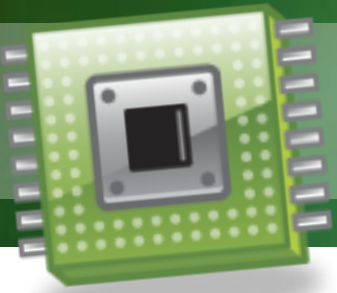
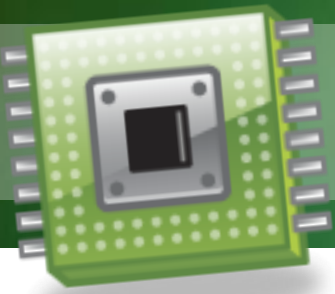


Introduction



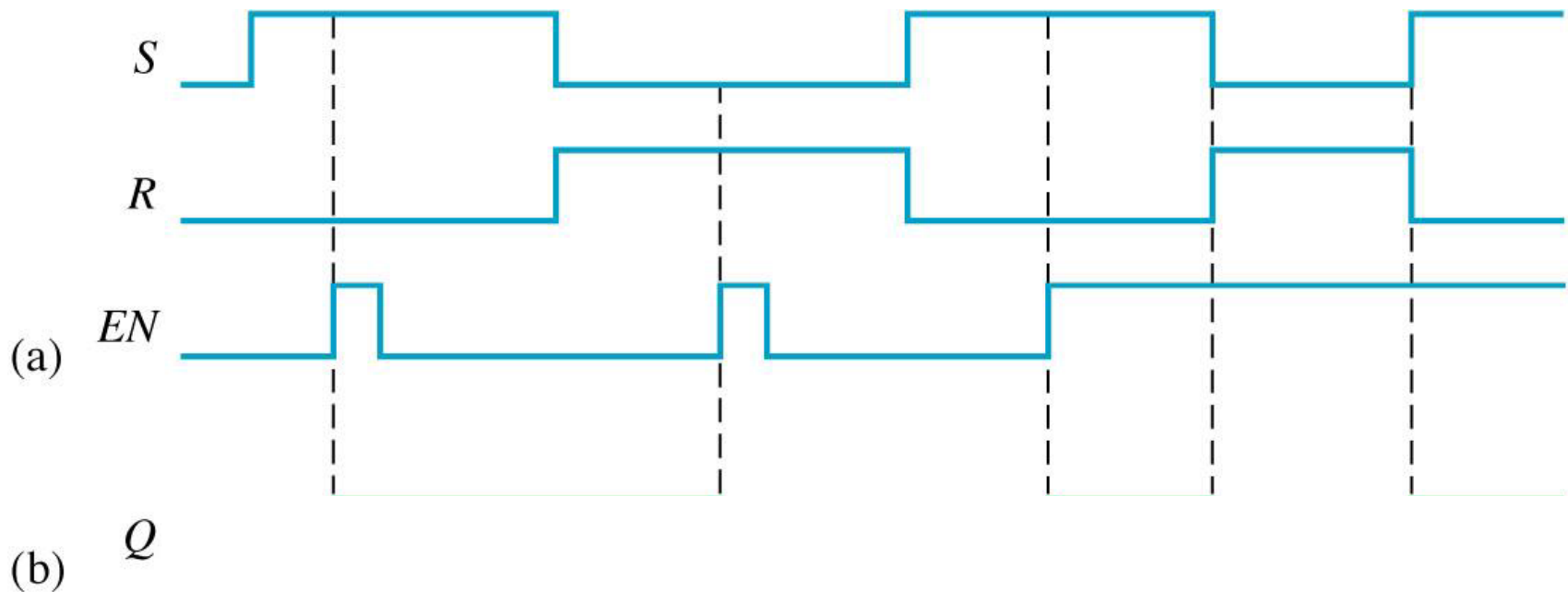
- Latches and flip-flops (FFs) are the basic single-bit memory elements used to build sequential circuit with one or two inputs/outputs, designed using individual logic gates and feedback loops.
- Latches are bistable devices whose state normally depend on asynchronous input.
- Edge triggered FFs are bistable devices which synchronous input whose state depend on the input only at triggering transition of clock pulse.

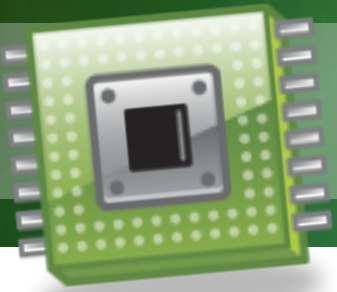


Gated S-R Latch (cont...)

Example 2:

Determine the Q output waveform if the inputs shown are applied to Gated S-R Latch that is initially RESET.





Gated D Latch (cont...)

■ Answer:

Preview from Notesale.co.uk
Page 19 of 19

