

```
cc -c srcfile3.c
clean:
rm -f myappl $(OBJS)
```

Predefined macros (make -p)

CC - C compiler command (cc)

FC - Fortran compiler command (f77)

CFLAGS - C compiler flags

FFLAGS - Fortran compiler flags

You can add macros to the command line : `make myappl CC=gcc`

Suffixes

predefined rules used by make (\$< stands for the file that triggered the rule i.e. the .c or .f file)

```
.SUFFIXES: .c .f
.c.o:
$(CC) $(CFLAGS) -c $<
.f.o:
$(FC) $(FFLAGS) -c $<
```

Use the command **make -p** to see a list of all predefined suffixes and macros.

Resources for learning about make

Managing Projects with make - Andrew Oram and Steve Talbott, O'Reilly & Associates, Inc 1991

GNU Make Manual - http://www.gnu.org/manual/make/html_mono/make.html

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